

Response to Action of 9/7/06 in case: 10/772,750

Atty Docket 42P18129

Amendments To the Specification

In the specification, please replace the Abstract with the following:

A processor including a plurality of logical processors, and an instruction set, the instruction set including one or more instructions which when executed by a first logical processor, causes the first logical processor to make a processor execution resource previously reserved for the first processor available to a second processor in the plurality of processors in response to the first logical processor being scheduled to enter an idle state.